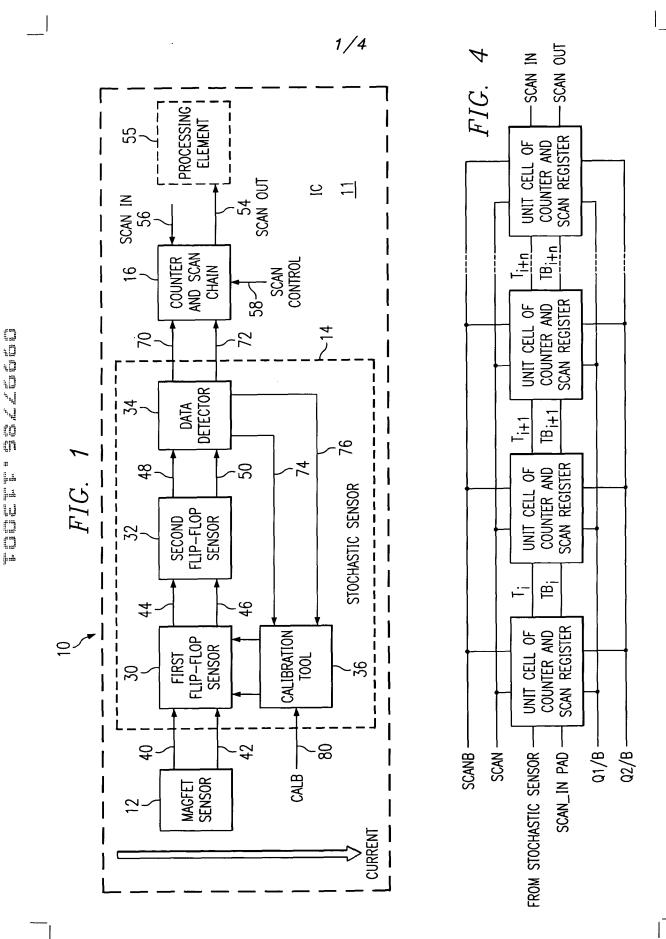
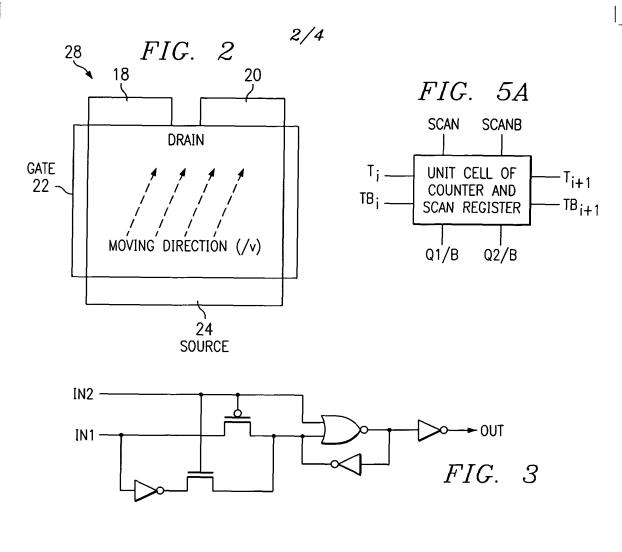
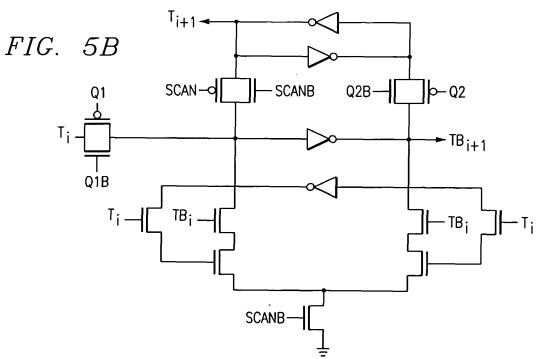
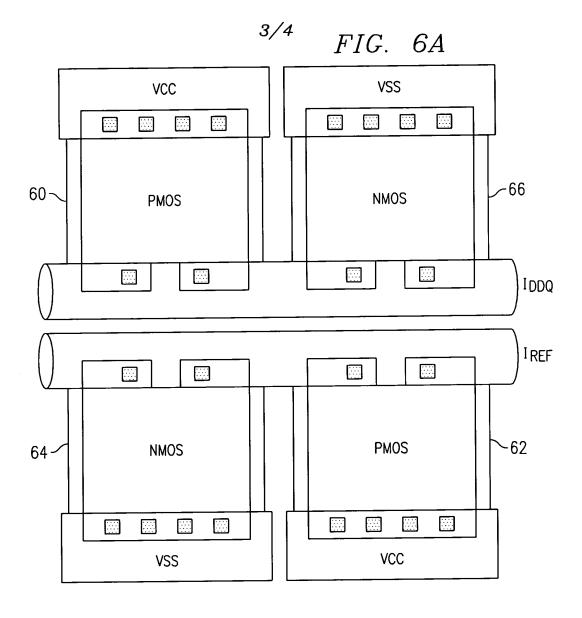
IRRENT IN AN INTEGRATED CIRCUIT ntor: Duncan M. Walker, et al. 017575 ress Mail No.: EL 759181505 US



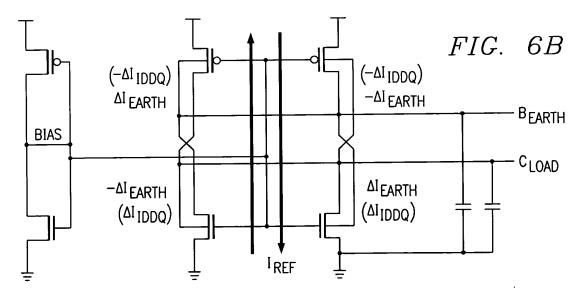
SYSTEM AND METHOD FOR DETECTING QUIESCENT URRENT IN AN INTEGRATED CIRCUIT ventor: Duncan M. Walker, et al. Express Mail No.: EL 759181505 US 017575.6-51







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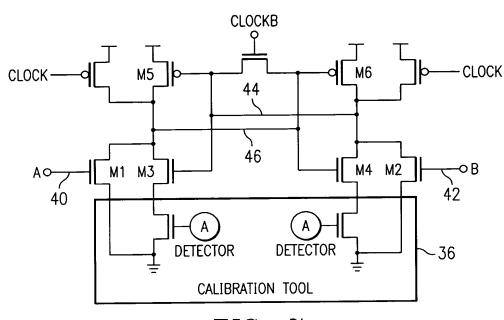


FIG. 7

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